

11/5/04

FORM PTO-1449 (MODIFIED)

Applicant: Wing K. Luk  
Robert H. Dennard  
Docket No.: YOR920030603US1  
Serial No.: Unassigned  
Filing Date: Concurrently Herewith  
Group: Unassigned

LIST OF PUBLICATIONS FOR  
APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT

U.S. PATENT DOCUMENTS


EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO

OTHER DOCUMENTS

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	

 Allen et al., "CMOS Analog Circuit Design," pg. 469 (1987).

Examiner J. Mondt

Date Considered

8/25/05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.